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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Garo J. Derderian et al.

Examiner: Mohsen Ahmadi

Patent No.: 7,312,163

Group Art Unit: 2812

Issue Date: December 25, 2007

Docket No: 1999-0970.00/US

Title: ATOMIC LAYER DEPOSITION METHODS, AND METHODS OF FORMING MATERIALS OVER SEMICONDUCTOR SUBSTRATES

Commissioner for Patents
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ATTN: Certificate of Correction Branch

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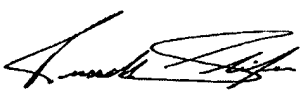
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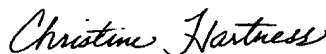
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Christine Hartness

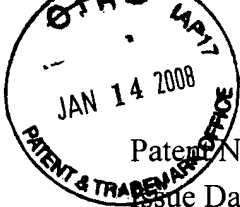
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REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

Date: January 11, 2008

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Russ Sliter
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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,312,163

Page (1) of 1

DATED : December 25, 2007

INVENTOR(S) : Derderian et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 11, in Claim 4, delete "SiCl₄." and insert - - SiCl₄. - -, therefor.

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